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01/15/02
751
JC971 U.S. PRO

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

N	APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
	10050002	01/15/2002	257	010	2841	

**APPLICANTS: Pavier Mark;

**CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/262,890 01/19/2001

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	<input type="checkbox"/> DO NOT PUBLISH	<input type="checkbox"/> RESCIND	
Foreign priority claimed	<input type="checkbox"/> yes	<input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes	<input type="checkbox"/> no	IR-1837
Verified and Acknowledged Examiners's initials			
TITLE : Back-to -back connected power semiconductor device package			
U.S. DEPT. OF COMM./PAT. & TM-PTC-436L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.	Print Fig.
TERMINAL		Primary Examiner		
DISCLAIMER		PREPARED F R ISSUE Application Examiner		
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